

## Installation and Commissioning Organization

# Detector Integration and Installation Coordinator Jim Christenson

Commissioning
Coordinator
Jonathan Kotcher

(started 5/99, moves to FNAL 9/99)

- detector installation in DØ assembly hall
- hardware integration
- advisory role in final subdetector fabrication

- establish/implement plan for bringing up experiment
- prepare system tests/ readout for hookup
- readout/shakedown of full detector prior to beam

Develop and implement integrated program for delivering detector



## Installation Sequence

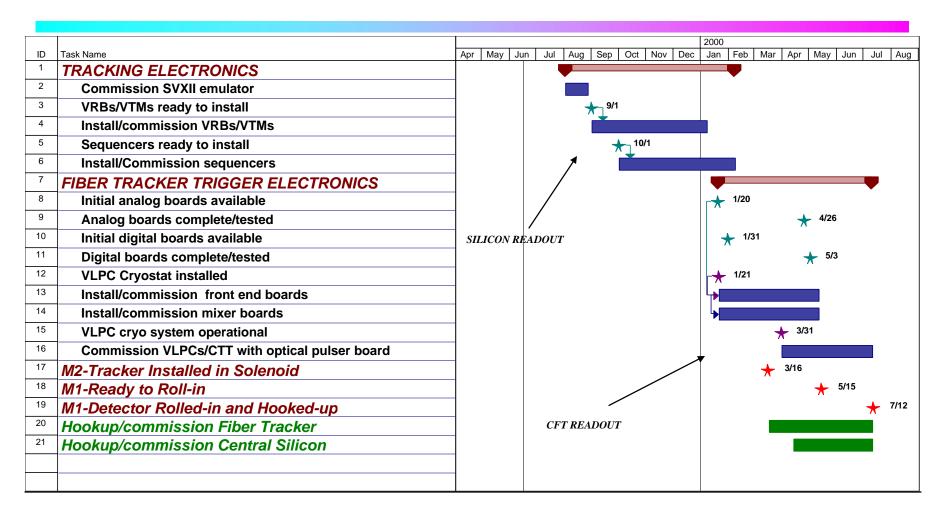
## Current Configuration: Solenoid/CPS Installed, ECS off platform, End Toroids on sidewalk

	Tool Monor				2000					
ID	Task Name	Duration	Start	Finish	Mar	Apr	May	Jun	Jul	Aug
1	Forward A and B-Layer Muon Detectors Installed	0 wks	3/27/00	3/27/00	*	3/27				
2	M2-Tracker Installed in Solenoid	0 wks	3/16/00	3/16/00	3/1	6				
3	Move ECS Calorimeter onto Platform	0.6 wks	3/17/00	3/21/00	■ĭ					
4	Move Platform West	1 wk	3/22/00	3/28/00	<b>L</b>					
5	M2-Muon End Toroids Installed on Platform	0 wks	4/3/00	4/3/00	<b>→</b>	1 <sup>4/3</sup>				
6	Move Platform East	0.4 wks	4/4/00	4/5/00	ի	L				
7	Mount B-Layer South MDT Octants Onto Supports	2 wks	4/6/00	4/19/00	Ì					
8	Mount B-Layer South Pixel Octants Onto Supports	2 wks	4/20/00	5/3/00			Ь			
9	Remove Shielding Wall	0.6 wks	5/15/00	5/17/00			<b>_</b>			
10	M1-Ready to Roll-in	0 wks	5/15/00	5/15/00			5	5/15		
11	Assemble/Install North EMC	3 wks	5/18/00	6/7/00			r I			
12	Assembly/Install South EMC	3 wks	5/25/00	6/14/00						
13	Roll Detector into Collision Hall	0.4 wks	6/16/00	6/19/00					ı	
14	M1-Detector Rolled-in and Hooked Up	0 wks	7/12/00	7/12/00					7.	/12
	1	1								

Well-defined series of steps

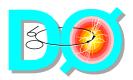


#### **Tracking Electronics Commissioning**



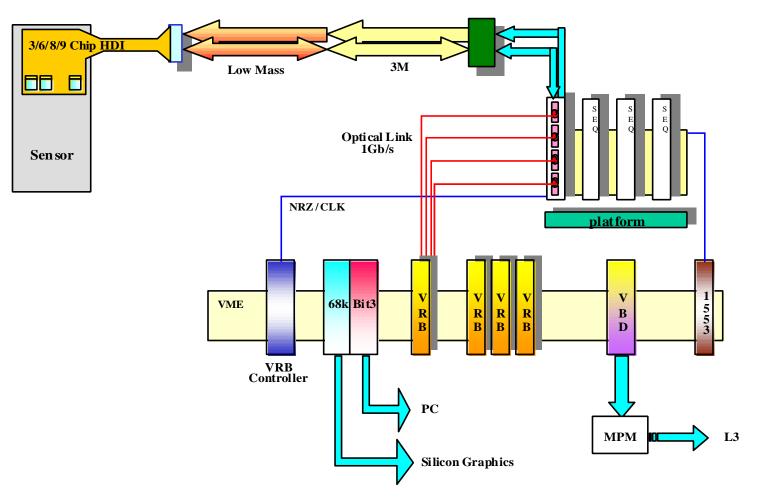
SVXII emulator, optical pulsers download MC events, diagnostic patterns:

Exercise full front end, all trigger levels



## **Silicon Data Pathway**

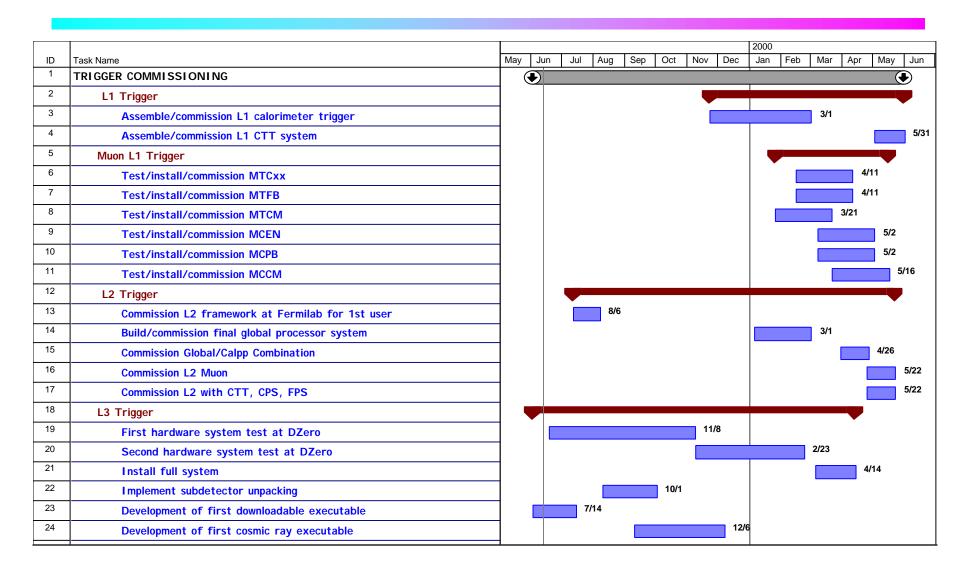
#### Silicon Read-Out Data Flow

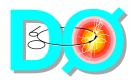


DoE Review June 1999



#### **Trigger Commissioning Schedule**





## **VLPC** Cassette Assignment

- VLPC cassette:
  - 1024-channel
  - ◆ 2 FE boards = two 4.5° trigger sectors
- Readout boards & crates:
  - ♦ 80 CFT Trigger, 76 CFT Stereo
  - 10 CPS Stereo
  - 32 FPS (North, South)
  - 2 spare cassettes
- CPS cassettes integrated with Fiber Tracker
- FPS cassette crates spatially located at east, west end of cryostat

